

FORM HDP-1449 (Based on Form PTO-1449)

ATTORNEY DOCKET NO.

SERIAL NO.

MP0074.C1

10/601,286

## PATENT AND TRADEMARK OFFICE

## INFORMATION DISCLOSURE CITATION

Use several sheets if necessary)

APPLICANT

Farbod ARAM et al.

FILING DATE

June 20, 2003

GROUP

UNKNOWN

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Ref. Desig.	Examiner's Initials	Document Number	Date (mm/dd/yyyy)	Name	Class/ Subclass	(If appropriate) Filing Date
	NH	6,597,562	07/22/2003	Hu et al.		

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	NH	JP 2-178963	07/11/1990	JAPAN		X (Abstract)	
		JP 64-84616	03/29/1989	JAPAN			X
		JP 59-105341	06/18/1984	JAPAN		X (Abstract)	
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		JP 02086160	03/27/1990	JAPAN		X (Abstract)	
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		JP 02191369	07/27/1990	JAPAN		X (Abstract)	
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		JP 54-101153	08/09/1979	JAPAN			X
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		JP 60-1825	01/08/1985	JAPAN			X
	NH	JP 60-4207	01/10/1985	JAPAN			X

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Ref. Desig.	Examiner's Initials	Document Number	Date (mm/dd/yyyy)	Country	Class/ Subclass	Translation	
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	NH	JP 60-92604	05/24/1985	JAPAN	—		X
	NH	JP 46-43172	12/21/1971	JAPAN	—		X
	NH	JP 59-91718	06/21/1984	JAPAN	—		X

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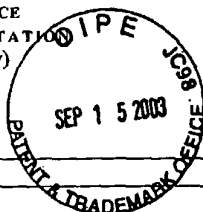
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		Shiba, Takashi et al. "Study on Transformer Ratio and Capacitance in Equivalent Circuit Model for SAW Transducer." 10 <sup>th</sup> Symposium on Ultrasonic Electronics (Tokyo 1989) <u>Japanese Journal of Applied Physics</u> , Vol. 29 (1990) Supplement 29-1, pp. 129-132.
		N. MONTH / DATE

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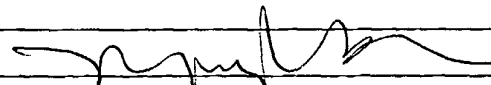
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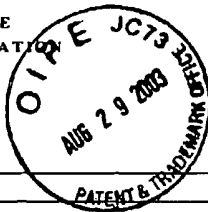
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	WPH	JP 1-120858	05/12/1989	JAPAN	—	X (Abstract)	
	PH	JP 3-257855	11/18/1991	JAPAN	—	X (Abstract)	

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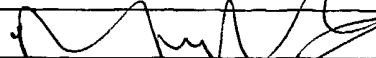


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3.		6,001,702	12-1999	Cook et al.		
4.		6,150,707	11-2000	Cook et al.		
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2.	UH	Runsheng He, Nersi Nazari and Sehat Sutardja, "A DSP Receiver for 1000BASE-T PHY," Submitted to ISSCC 2001. <i>Date / Month / year missing</i>
3.	↓	S. Sutarja and P.R. Gray, "A pipelined 13-bit, 250-ks/s, 5-V, analog-to-digital converter," IEEE J. Solid-State Circuits, vol. 23, pp. 1316-1323, Dec. 1988.
4.	↓	S.H. Lewis and P.R. Gray, "A pipelined 5-Msample/s 9-bit analog to digital converter," IEEE J. Solid-State Circuits, vol. SC-22, pp. 954-961, Dec. 1987.
5.	UH	S.H. Lewis, R. Ramachandran "A 10-b 20-Msample/s Analog-to-Digital Converter," IEEE J. Solid-State Circuits, vol. 27, pp. 351-358, March 1992.

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